

ANKASYS FPGA-SYS-4D

ALTERA
now part of Intel

ZYNQ

- ➔ Anka Microelectronic Systems offers FPGA trainings, which covers everything you need to get you started with FPGA based digital system design.
- ➔ The ANKASYS FPGA-SYS-4D is an FPGA based digital system design course, which uses Verilog/SystemVerilog language to introduce you the digital design. The FPGA system design is based on either Xilinx or Intel FPGAs.

Training Content

DAY 1

- What is and FPGA, the Legacy and Modern Systems
- FPGA and ASIC Comparison
- Fundamentals of Digital Electronics
- Metastability and its Mitigation Techniques
- Finite State Machine Design

DAY 2

- Clocks, Clock Tree and Buffers
- Memories, Buffers and FIFOs
- Pipeline Structures
- Bus protocols and ARM AMBA APB, AHB and AXI

DAY 3

- Timing Arcs and Timing Constraints
- Timing Exceptions, False Paths, Multi-cycle Paths
- The SDC Format
- Physical Constraints and Its Applications

DAY 4

- Vivado/Quartus Tool and System Design with Xilinx Zynq/ Intel SOC FPGAs
- Hardware Design and Debug
- Software Design
- Hardware-Software Co-Design and Validation
- Power Analysis and Power Optimization Techniques

Anka Microelectronic Systems
İTÜ Ayazağa Kampüsü, Arı Teknokent 8,
Reşitpaşa Mah. Katar Cad., Öğrenci Otomasyon Sit. No 2/11/4
34467, Sarıyer, İstanbul, Turkey
Tel: +90 (532) 780 98 62 - www.ankasys.com - info@ankasys.com

