ANKASYS SPACEWIRE UVCS
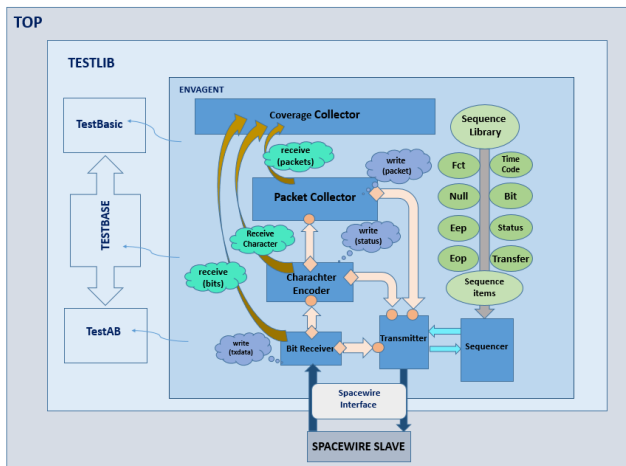
Anka Microelectronic Systems offers a unique SpaceWire verification package, where both the universal verification component and corresponding integration and training services are included in a single so called UVCS (Universal Verification Component and Services) package.

Implementation is done in pure SystemVerilog language. No third party language or script is needed to integrate the IP.

The complete component hierarchy inside the verification IP and corresponding test codes (sequences and tests) are all driven from UVM standard library. No customized vendor specific code.

Key IP Features

- Easy integration and user interface.
- Decodes SpaceWire D-S(data strobe) encoded signals and provides signal level synchronization.
- Extracts all SpaceWire characters and codes: FCT, NULL, EOP, EEP, TIMECODE, NCHAR.
- Provides character synchronization.
- Handles SpaceWire exchange layer : Link initialization, error detection and recovery.
- Supports both single and burst transfers.
- Ready-to-use Systemverilog Assertion (SVA) based protocol checkers.
- Random and selectable pre/post delay injections.
- Pre-defined Systemverilog covergroups for coverage collection.
- Pre-defined SpaceWire character, code and package level sequences.
- Thanks to the layered implementation approach, emits all SpaceWire signal, character and packet level transaction at the VIP (Verification IP) interface for easy analysis, reporting and debugging of SpaceWire designs.

Available Services

- Integration Support
- Application Support
- Customization
- Maintenance

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