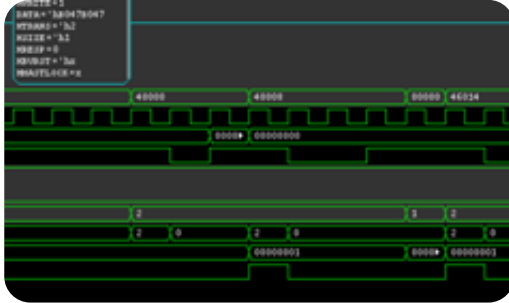


REF: FUNCVERIF - 100101



- Location: The Republic of Ireland.
- Starting Date : ASAP
- Employment Type: Contract

Required Skills

- SystemVerilog for verification knowhow
- SystemVerilog and/or PSL assertions
- Object oriented design methodology background
- Coverage driven constrained random verification methodology experience
- OVM/UVM experience at both IP and SOC level
- Synopsys VCS simulator experience
- Transaction level modelling experience
- PCIe experience

Required Profile

- Should be an EU/EEA citizen without any restrictions to be able to work in the Republic of Ireland.
- Should have a minimum of 3 years of relevant experience
- Should have a degree in electrical engineering or computer science
- Should have sense of responsibility & able to take initiatives



Anka Microelectronic Systems