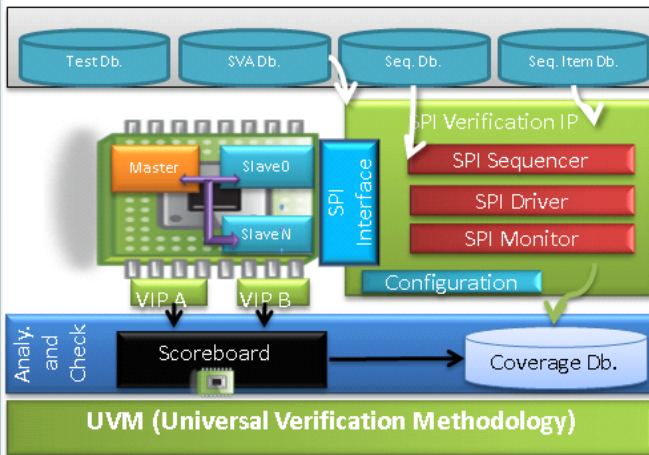


ANKASYS SPI UVCS



➔ Anka Microelectronic Systems offers a unique SPI verification package, where both the universal verification component and corresponding integration and training services are included in a single so called UVCS (Universal Verification Component and Services) package.

➔ Implementation is done in pure Systemverilog language. No third party language or script is needed to integrate the IP

➔ The complete component hierarchy inside the verification IP and corresponding test codes (sequences and tests) are all driven from UVM standard library. No customized vendor specific code.

Key IP Features

- Easy integration and user interface.
- All four SPI modes are supported.
- Multi slave configuration with unlimited number of slaves
- Automatic driver and monitor thread termination support via a single enabling bit.
- Supports both single and burst transfers
- Ready-to-use Systemverilog Assertion (SVA) based protocol checkers.
- Random and selectable pre/post delay injections.
- Pre-defined Systemverilog covergroups.
- Pre-defined byte, word and frame sequences
- Multi-slave addressing for the same transfer

Available Services

- Integration Support
- Application Support
- Customization
- Maintenance



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