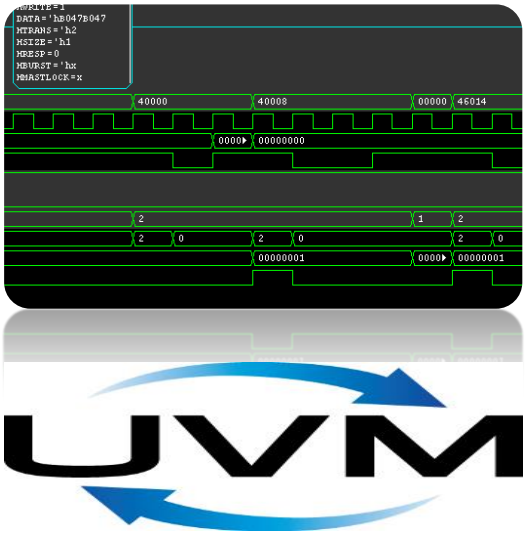
ANKASYS UVM-4D

ANKASYS TRAININGS



- Anka Microelectronic Systems offers verification trainings, which covers everything you need to get you started with advanced verification techniques.
- ANKASYS UVM-4D is a 4-Day Universal Verification Methodology (UVM) Training, which has all the required tools to get you started with your first UVM experience.
- ANKASYS UVM-4D gives you the possibility to revisit the topics you already know from a different perspective or learn and experience new and exciting ones.

Training Content

DAY 1

1. Introduction to metric/coverage driven constrained random verification.
   - Randomization and the legacy directed approach
   - Constraints and their applications at different levels of UVM testbench
   - Run-time metrics and SystemVerilog coverage, how and when to collect coverage
2. Introduction to object oriented design methodology
   - Class, objects and their properties
   - Data hiding, inheritance and polymorphism
   - Factory design pattern
   - Proxy design pattern
3. Introduction to verification components
   - Interfaces, drivers, monitors, agents etc.
   - Sequence items, sequences and tests.
4. Static and dynamic domains of a testbench
   - The top level module
   - Communication of the two domains

DAY 2

1. Transaction level communication
   - Transaction level modeling
   - The UVM sequence item, sequences and virtual sequences
   - The layered UVM sequences
2. UVM verification components
   - The UVM sequencer, driver, monitor and scoreboards
   - The UVM agents and environments
   - The UVM test

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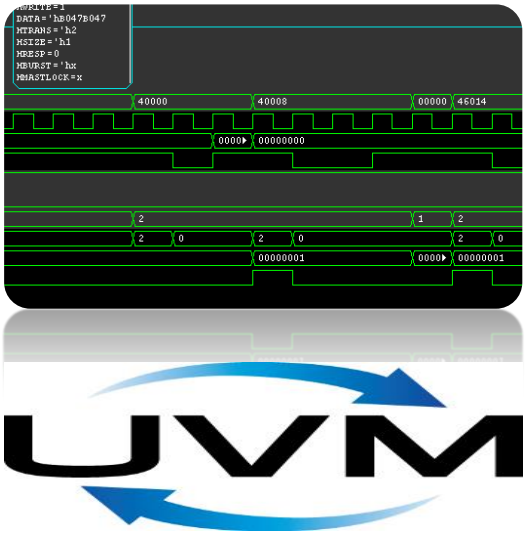

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Training Content

DAY 3

1. Verification environment management and configuration
   - Component configurations and config_db
   - Interfaces and virtual interface handling
   - Test and sequence configuration
2. The UVM register layer class
   - Introduction to XML based register handling
   - Generation of the UVM register layer class
   - Memory maps, memories, registers and fields
   - The UVM register callbacks

DAY 4

1. Verification planning and management
   - The executable vPlan and verification specification
   - Regression management
2. Wrap up

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