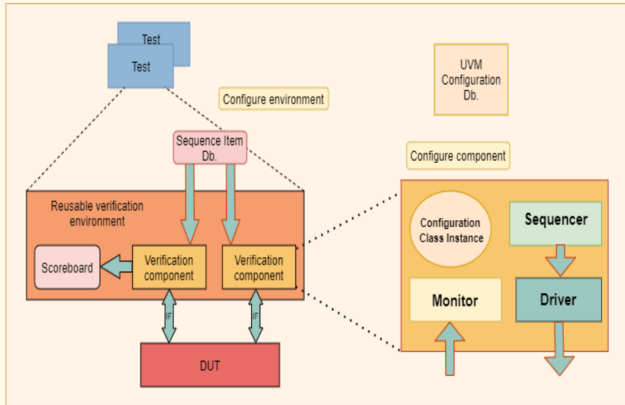# ANKASYS BASE VIP

ANKASYS VIP FAMILY

As digital designs are getting more complex, and production and fabrication and re-design costs are high, industry is in need of more reliable/reusable verification approaches to verify them.

UVM is one of the leading verification approaches, a standardized methodology to verify digital designs for FPGA/ASIC/SOC. UVM hierarchy has many components in it and the relations between these components are fairly complex. ANKASYS offers Verification IPs, so called ANKASYS VIPs. ANKASYS VIPS benefit from UVM features and they reduce the complexity of UVM hierarchy.

ANKASYS Verification IPs include both the universal verification component and corresponding integration with training services in a single pre-packaged library. Its ease and parametric structure provides verification engineers highly customizable ready to use VIP base to speed up verification process.

ANKASYS Verification IP Product Family includes

- ANKASYS BASE VIP

Protocol specific VIPs:
- ANKASYS SPI VIP
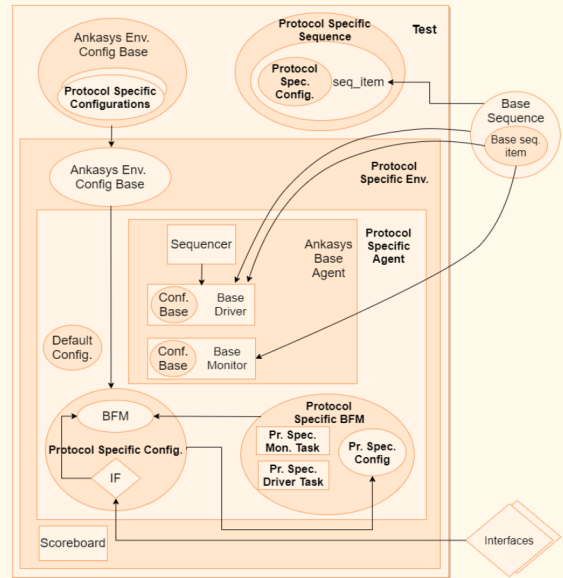- ANKASYS I2C VIP
- ANKASYS UART VIP

ANKASYS Verification IPs are implemented in pure SystemVerilog language. No third-party language or script is needed for integration. The complete component hierarchy inside the verification IP and corresponding test codes (Sequences and tests) are all driven from UVM standard library.

ANKASYS BASE VIP

ANKASYS BASE VIP offers a VIP infrastructure with highly reusable and simplified structures.
Any new protocol specific VIP can be developed by extending ANKASYS BASE VIP.
Therefore, the complete verification environment keeps a higher level of consistency with reduced development efforts.

Key Features

- Easy integration and user-friendly interface
- Simplified Driver/Monitor components for BFM infrastructure
- Highly reusable UVM Testbench and Environment
- Full support for UVM 1.1 and later versions
- Runs on industry leading simulators

Available Services

- Integration Support
- Application Support
- Customization
- Maintenance

Deliverables

- ANKASYS BASE VIP Pre-packaged library
- Reference Manual and Sample VIP Implementation guide
- Sample VIP* implementation codes

*Sample VIP is a simple protocol specific VIP example which is extended from ANKASYS BASE VIP.